

## REMARKS

This Amendment is submitted in response to the Examiner's Action mailed August 26, 2004, with a shortened statutory period of three months set to expire November 26, 2004. Claims 1-36 are currently pending. With this amendment, claims 1-2, 6-8, 11-28, and 32-33 have been amended.

Applicants have amended the claims to describe a computer system instead of a data processing system.

Applicants have also amended claims 1, 17, and 27 to describe a default port and a second port. The data stream is destined for the default port. An application directs the data stream to the default port such that the data stream is intended by the application to be sent from the default port.

Responsive to a request from a terminal, a signal is sent from a processor to a hardware switch to redirect the data stream from the default port to a second port. The data stream is packetized for transmission over the second port. The packetized data stream is sent from the second port instead of the default port. The data stream is redirected from the default port to the second port without modifying the application.

Applicants have amended claims 7, 23, and 33 to describe redirecting a data stream that is destined for a default port. An application directs the data stream to the default port. The data stream is intended by the application to be sent from the default port. A request is received to redirect the data stream to a desired port instead of the default port.

A service processor sends a signal to hardware switch to redirect the data stream. The hardware switch is coupled between the default port and an I/O processor that is included within the computer system.

Packetized data is sent from the desired port instead of the default port. The data stream is redirected from the default port to the desired port without modifying the application.

The Examiner rejected claims 1-36 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent 6,055,224 issued to *King*. This rejection, as it might be applied to the claims as amended, is respectfully traversed.

Figure 9a of *King* depicts a data transceiver. The data transceiver is included within a port that has an Ethernet bus output and a 10 Base T line output. A switch is coupled to the Ethernet bus and the 10 Base T line. Data can be transmitted via this single port out from either the Ethernet bus using the Ethernet standard or out from the 10 Base T line using the 10 Base T line standard. The Ethernet bus includes an Ethernet processor. The Ethernet processor is coupled between the Ethernet bus output and the switch. The switch is used to select either the bus or the line as the output of the port.

Applicants claim an application that directs a data stream to a default port where the data stream is intended to be sent from the default port. A signal is sent to a hardware switch to redirect the data stream from the default port to a second port. The data stream is then sent from the second port instead of from the default port as intended by the application. The data stream is redirected from the default port to the second port without modifying the application.

*King* teaches one port that has two outputs, an Ethernet bus and a 10 Base T line. One of these outputs is selected to use to output the data from the port. *King* does not teach an application that directs the data stream to the default port such that the data stream is intended by the application to be sent from the default port where the data stream is redirected from the default port to the second port without modifying the application.

Applicants also claim the hardware switch is coupled between the default port and an I/O processor in the computer system. *King* teaches an I/O processor, i.e. Ethernet processor, that is located between the Ethernet bus and the switch. Applicants claim the switch being coupled between the port and the I/O processor. *King* does not teach a switch that is coupled between a port and an I/O processor.

*King* does not teach an application that directs a data stream to a default port such that the data stream is intended by the application to be sent from the default port where the data stream is redirected from the default port to a desired port by a hardware switch without modifying the application and where the hardware switch is coupled between the default port and an I/O processor included within the computer system.

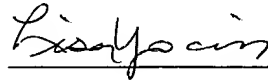
*King* does not anticipate Applicants' claims because *King* does not teach an application that directs the data stream to a default port such that the data stream is

intended by the application to be sent from the default port where the data stream is redirected from the default port to the second port without modifying the application, and *King* does not teach a hardware switch that is coupled between a port and an I/O adapter in the computer system.

The Examiner is invited to call the undersigned at the below-listed telephone number if in the opinion of the Examiner such a telephone conference would expedite or aid the prosecution and examination of this application.

DATE: 11.22.04

Respectfully submitted,



Lisa L.B. Yociss  
Reg. No. 36,975  
Yee & Associates, P.C.  
P.O. Box 802333  
Dallas, TX 75380  
(972) 385-8777  
Attorney for Applicants